

ABSTRACT

A method and associated apparatus are provided for improving
5 the performance of a high speed data bus, such as a memory bus, using
selectively activated receiver and driver pairs. Each receiver and driver pair
may be selectively activated to permit data communication on a segment of
the high speed data bus coupled to the activated receiver and driver pair.
Each receiver and driver pair may also be deactivated, thereby
10 disconnecting at least a respective segment of the high speed data bus, so
that communicating system components may be connected in a
substantially stubless environment.

FOI b7E b7C b7D b7F b7G b7H b7J b7K b7L b7M b7N b7O b7P b7Q b7R b7S b7T b7U b7V b7W b7X b7Y b7Z